IIIT Hyderabad LEEE Syllabus

Computer Science and Engineering Exam

	IIIT Hyderabad LEEE CSE Syllabus
General Aptitude	• **Verbal Aptitude** – Basic English grammar, Tenses, articles, adjectives, prepositions, conjur Basic vocabulary: words, idioms, and phrases in context Reading and c • **Data interpretation** – Data graphs (bar graphs, pie charts, and other graphs representir Numerical computation and estimation: ratios, percentages, powers, exponents and lo Mensuration and geometry Elementary statistics • **Analytical Aptitude** – Logic deduction and induction, Analogy N • **Spatial Aptitude** – Transformation of shapes: translation, rotation, scaling, mirroring, assem 2 and 3 dimensions
Discrete Mathematics	• Algorithm • Binary • Discrete Mathematics • Logic
Digital Logic	• Number System and Boolean Algeb • Minimization Techniques • Combinational Circuits • Sequential Circuits • Memory Devices
Computer Organization	• Basic Computer Instructions • Instruction Design and Format • Computer Arithmetic • Microprogrammed Control • Memory Organization
Data Structures	• Array • Pointer • Structure • Linked List

	• Stack • Queue • Graph • Searching • Sorting • Programs
C-Programming	• Arrays • Conditional statements and switch • File handling • Variables and handling datatypes • Structures • Functions • Pointers • System-level operations using inbuild h • Building functions of Strings

Electronics and Communications Engineering Exam

	IIIT Hyderabad LEEE ECE Syllabus
General Aptitude	• **Verbal Aptitude** – Basic English grammar, Tenses, articles, adjectives, prepositions, conjun Basic vocabulary: words, idioms, and phrases in context Reading and • **Data interpretation** – Data graphs (bar graphs, pie charts, and other graphs representi Numerical computation and estimation: ratios, percentages, powers, exponents and lo Mensuration and geometry Elementary statistics • **Analytical Aptitude –** Logic deduction and induction, Analogy Nu • **Spatial Aptitude –** Transformation of shapes: translation, rotation, scaling, mirroring, asse in 2 and 3 dimensions
Digital Logic	• Number System and Boolean Algeb • Minimization Techniques • Combinational Circuits • Sequential Circuits • Memory Devices
Circuit Theory	• Network analysis using KCL and KV

and Networks	• Laplace transform • Network Theorems • Phasor diagrams • Transient Analysis of First-order circ • Magnetic Coupled circuits • Resonance • Two-port Networks
Electronic Devices and Circuits	• Semiconductor diode: I-V characteristics for forward and revers • I-V characteristics of LED, solar cell, photodiode, and Zener diode; Zer • Junction transistor, characteristics of a transistor, transistor action; transistor as an am • Logic gates (OR, AND, NOT, NAND and • Transistor as a switch. • Semiconductors
Signals and Systems	• Discrete-Time Signals • Fourier Series and its Application • LTI Systems • Representation of Continuous • Discrete-Time Signals and Sampling Th